

U.S. Department of Commerce, Patent and Trademark Office	Application No.: Unknown
	Filing date: October 2, 2001
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Craft, et al.
	Group Art Unit: Unknown
"NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS"	Examiner name: Unknown
Express Mail No.: ET203107806US	Attorney Docket No.

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>MS</i>	AA	5,280,477	1/18/94	Trapp	370	85.1	
	AB	5,448,566	9/5/95	Richter, et al.	370	94.1	
	AC	5,634,127	5/27/97	Cloud, et al.	395	680	
	AD	5,751,715	5/12/98	Chan, et al.	370	455	
	AE	4,991,133	2/5/91	Davis, et al.	364	900	
	AF	5,163,131	11/10/92	Row, et al.	395	200	
	AG	5,212,778	5/18/93	Dally, et al.	395	400	
	AH	5,289,580	2/22/94	Latif, et al.	395	275	
	AI	5,303,344	4/12/94	Yokoyama, et al.	395	200	
	AJ	5,412,782	5/2/95	Hausman, et al.	395	250	
	AK	5,485,579	1/16/96	Hitz, et al.	395	200.12	
	AL	5,506,966	4/9/96	Ban	395	250	
	AM	5,511,169	4/23/96	Suda	395	280	
<i>MS</i>	AN	5,548,730	8/20/96	Young, et al.	395	280	

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
<i>MS</i>	AO	PCT/US98/08719 (WO98/50852)	11/12/98	International				
<i>MS</i>	AP	PCT/US97/17257 (WO98/19412)	5/7/98	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>MS</i>	AQ	Internet pages entitled: DART Fast Application - Level Networking Via Data-Copy Avoidance, by Robert J. Walsh, printed 6/3/99.
<i>MS</i>	AR	Internet pages of InterProphet entitled: Frequently Asked Questions, by Lynne Jolitz, printed 6/14/99.
<i>MS</i>	AS	Internet pages entitled: Technical White Paper - Xpoint's Disk-to-LAN Acceleration Solution for Windows NT Server, printed 6/5/97.

Examiner <i>MS</i>	Date Considered 6/29/06
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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"NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS"	Examiner name: Unknown
Express Mail No.: ET203107806US	Attorney Docket No. ALA-020

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>ML</i>	BA	5,588,121	12/24/96	Reddin, et al.	395	200.15	
	BB	5,590,328	12/31/96	Seno, et al.	395	675	
	BC	5,592,622	1/7/97	Isfeld, et al.	395	200.02	
	BD	5,634,099	5/27/97	Andrews, et al.	395	200.07	
	BE	5,642,482	6/24/97	Pardillos	395	200.2	
	BF	5,671,355	9/23/97	Collins	395	200.2	
	BG	5,692,130	11/25/97	Shobu, et al.	395	200.12	
	BH	5,699,317	12/16/97	Sartore, et al.	395	230.06	
	BI	5,701,434	12/23/97	Nakagawa	395	484	
	BJ	5,749,095	5/5/98	Hagersten	711	141	
	BK	5,752,078	5/12/98	Delp, et al.	395	827	
	BL	5,758,084	5/26/98	Silverstein, et al.	395	200.58	
	BM	5,758,089	5/26/98	Gentry, et al.	395	200.64	
	BN	5,758,186	5/26/98	Hamilton, et al.	395	831	
	BO	5,758,194	5/26/98	Kuzma	395	886	
<i>ML</i>	BP	5,790,804	8/4/98	Osborne	395	200.75	

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

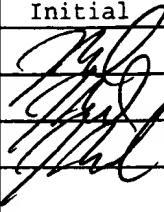
<i>ML</i>	BQ	Jato Technologies Internet pages entitled: Network Accelerator Chip Architecture, twelve-slide presentation, printed 8/19/98.
<i>ML</i>	BR	EETIMES article entitled: Enterprise System Uses Flexible Spec, by Christopher Harrer and Pauline Shulman, dated August 10, 1998, Issue 1020, printed 11/25/98.
<i>ML</i>	BS	Internet pages entitled: iReady About Us and iReady Products, printed 11/25/98.

Examiner <i>ML</i>	Date Considered <i>6/29/06</i>
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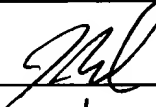
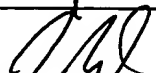
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	CA	5,812,775	9/22/98	Van Seters, et al.	395	200.43	
	CB	5,056,058	10/8/91	Hirata, et al.	364	900	
	CC	5,678,060	10/14/97	Yokoyama et al.	709	212	
	CD						
	CE						
	CF						
	CG						
	CH						
	CI						
	CJ						
	CK						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	CL	Internet pages entitled: Smart Ethernet Network Interface Card, which Berend Ozceri is developing, printed 11/25/98.
	CM	Internet pages entitled : Hardware Assisted Protocol Processing, which Eugene Feinberg is working on, printed 11/25/98.
	CN	Internet pages of XaQti Corporation entitled: Giga POWER Protocol Processor Product Preview, printed 11/25/98.
	CO	Internet pages of Xpoint Technologies www.xpoint.com web site (5 pages), printed 12/19/97.
	CP	Internet pages relating to iReady Corporation and the iReady Internet Tuner Module, printed 11/2/98.
	CQ	Internet pages entitled: Asante and 100BASE-T Fast Ethernet, printed 5/27/97.
	CR	Internet pages entitled: A Guide to the Paragon XP/S-A7 Supercomputer at Indiana University, printed 12/21/98.

Examiner

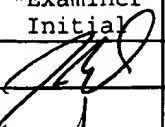
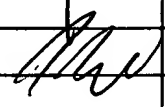
Date Considered

6/29/06

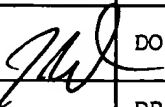
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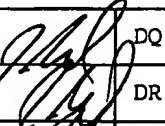
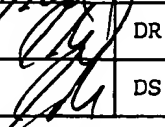
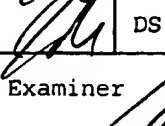

U.S. Patent Documents

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	DA	5,566,170	10/15/96	Bakke, et al.	370	60	
	DB	6,009,478	12/28/99	Panner, et al.	710	5	
	DC	6,034,963	3/07/00	Minami, et al.	370	401	
	DD	5,878,225	03/02/99	Bilansky et al.	395	200.57	
	DE	5,991,299	11/23/99	Radogna et al.	370	392	
	DF	5,930,830	07/27/99	Mendelson et al.	711	171	
	DG	6,061,368	05/09/00	Hitzelberger	370	537	
	DH						
	DI						
	DJ						
	DK						
	DL						
	DM						
	DN						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
	DO	PCT/US98/14729 (WO99/04343)	1/28/99	International				
	DP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	DQ	60/053,240 (U.S. Provisional Application), by Jolitz et al. (listed filing date July 18, 1997).
	DR	Zilog Product Brief entitled "Z85C30 CMOS SCC Serial Communication Controller", Zilog Inc., 3 pages (1997).
	DS	Andrew S. Tanenbaum, Computer Networks, Third Edition, ISBN 0-13-349945-6 (1996).
Examiner 	Date Considered 6/29/06	

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<i>MS</i>	EA	4,336,538	12/28/92	Johnson et al.	364	200	
	EB	5,097,442	03/17/92	Ward et al.	365	78	
	EC	5,629,933	05/13/97	Delp et al.	370	411	
	ED	5,664,114	09/02/97	Krech, Jr. et al.	395	200.64	
	EE	5,794,061	08/11/98	Hansen et al.	395	800.01	
	EF	5,802,580	09/01/98	McAlpice	711	149	
	EG	5,815,646	09/29/98	Purcell et al.	395	163	
<i>MS</i>	EH	5,771,349	06/23/98	Picazo, Jr. et al.	395	188.01	
	EI						
	EJ						
	EK						
	EL						

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
<i>MS</i>	EM	PCT/US98/24943 (WO 00/13091)	11/20/98	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>MS</i>	EO	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
<i>MS</i>	EP	VT8501 Apollo MVP4 Documentation, VIA Technologies, Inc., pages i-iv, 1-11, cover and copyright page, revision 1.3 (Feb. 1, 2000).
	EQ	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
	ER	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996.
<i>MS</i>	ES	Internet pages directed to; Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.

Examiner

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U.S. Patent Documents

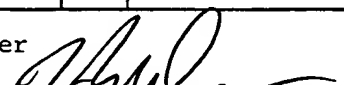
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
FA						
FB						
FC						
FD						

Foreign Patent Documents

							Translation	
	Document Number	Date	Country	Class	Subclass	Yes	No	
FE								

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

FF	Internet web pages from "Adaptec.com" website directed to the Adaptec, AEA-7110C iSCSI Host Bus Adapter and about Adaptec's IP storage activities, 11 pages, downloaded and printed 10/1/01.
FG	Internet web pages from "iSCSIhba.com" website directed to JNI iSCSI HBAs including the "FCE-3210/6410", 10 pages, downloaded and printed 10/1/01.
FH	Internet web pages from the "iSCSI Storage.com" website that mention an Emulex HBA, 2 pages, downloaded and printed 10/1/01.
FI	Internet web pages from the "iSCSIhba.com" website that mention QLogic HBAs including the "SANblade 2300 Series", 8 pages, downloaded and printed 10/1/01.
FJ	

Examiner 	Date Considered 6/29/06
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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/970,124
JAN 15 2002 INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 2, 2001
	Inventors: Craft, et al.
	Group Art Unit: 2154
"NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS"	Examiner name: Unknown
	Attorney Docket No. ALA-020

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>Paul</i>	A	5,913,028	6/15/99	Wang, et al.	395	200.33	

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
<i>Paul</i>	B	WO 99/65219 (PCT/US99/13184)	6/10/99	International				
	C	WO 01/05107 A1 (PCT/US00/19006)	7/13/00	International				
	D	WO 01/05116A2 (PCT/US00/19243)	7/13/00	International				
	E	WO 01/05123 A1 (PCT/US00/18976)	7/12/00	International				
<i>Paul</i>	F	WO 01/04770A2 (PCT/US00/18939)	7/11/00	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>Paul</i>	G	Internet pages from iReady News Archives entitled "iReady Rounding Out management team with two key executives," 2 pages, (printed 11/28/98).
	H	Toshiba, "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady's Design", 3 pages, Press release 10/14/98, downloaded 11/28/98.
	I	Internet pages entitled "iReady Products" from website http://www.ireadyco.com/products.html , 2 pages, printed 11/25/98.
	J	iReady News Archives. Toshiba, iReady shipping Internet Chip, 1 page, printed 11/28/98.
	K	Internet site www.interprophet.com , 17 pages, printed 3/1/00.
<i>Paul</i>	L	The I-1000 Internet Tuner Features, iReady Corporation, 2 pages, date unknown.

Examiner

Date Considered

6/29/06

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U.S. Department of Commerce Patent and Trademark Office	Application No.: 09/970,124
JAN 15 2002	Filing date: October 2, 2001
INFORMATIONAL DISCLOSURE STATEMENT BY APPLICANT	Inventors: Craft, et al.
	Group Art Unit: 2154
"NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS"	Examiner name: Unknown
	Attorney Docket No. ALA-020

U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
M						

Foreign Patent Documents

							Translation	
	Document Number	Date	Country	Class	Subclass		Yes	No
N								

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

O	Internet pages from website http://www.ireadyco.com/about.html , 3 pages, downloaded 11/2/98.
P	IReady News Archives, "Revolutionary Approach to Consumer Electronics Internet Connectivity Funded", San Jose, CA. 2 pages, 11/20/97, downloaded and printed 11/02/98.
Q	IReady News Archives, "Seiko Instruments Inc. (SII) Introduces World's First Internet-Ready Intelligent LCD Modules Based on IReady Technology", 2 pages 10/26/98, downloaded and printed 11/2/98.
R	NEWSwatch - IReady Internet Tuner to Web Enable Devices, November 5, 1996. 2 pages, printed 11/02/98.
S	David Lammers, EETimes, 06/13/97, "Tuner for Toshiba, Toshiba taps iReady for Internet tuner," 2 pages, printed 11/02/98.
T	Internet pages entitled: Comparison of Novell Netware and TCP/IP Protocol Architectures, by Janique S. Carbone, 19 pages, July 16, 1995, downloaded and printed 4/10/98.
U	
V	
W	

Examiner	Date Considered 6/25/06
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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/970,124
	Filing date: October 2, 2001
SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Peter K. Craft et al.
	Group Art Unit: 2154
PIPE NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS	Examiner name: Unknown
MAR 29 2002	Attorney Docket No. ALA-020

U.S. Patent Documents

Examiner Initials	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	AA	6,173,333	07/18/97	Jolitz et al.		
	AB	2001/0025315A1		Jolitz		01/10/01
	AC	2001/0004354A1		Jolitz		01/10/01
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
	AL					

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Foreign Patent Documents

							Translation	
	Document	Date	Country	Class	Subclass		Yes	No
	AM							
	AN							
	AO							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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	AQ	
	AR	
	AS	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 02, 2001
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<i>ML</i>	AA	5,701,516	12/23/97	Cheng et al.	395	842	
	AB	5,809,328	09/15/98	Nogales et al.	395	825	
	AC	5,931,918	08/03/99	Row et al.	709	300	
	AD	5,935,205	08/10/99	Murayama et al.	709	216	
	AE	5,937,169	08/10/99	Connery et al.	395	200.8	
	AF	5,941,969	08/24/99	Ram et al.	710	128	
	AG	5,941,972	08/24/99	Hoese et al.	710	129	
	AH	5,950,203	09/07/99	Stakuis et al.	707	10	
	AI	5,996,024	11/30/99	Blumenau	709	30	
	AJ	6,005,849	12/21/99	Roach et al.	370	276	
	AK	6,016,513	01/18/00	Lowe	709	250	
	AL	6,026,452	02/15/00	Pitts	710	56	
	AM	6,044,438	03/28/00	Olnowich	711	130	
	AN	6,047,356	04/04/00	Anderson et al.	711	129	
	AO	6,057,863	05/02/00	Olarig	345	520	
	AP	6,065,096	05/16/00	Day et al.	711	114	
	AQ	6,141,705	10/31/00	Anand et al.	710	15	
	AR	6,246,683	06/12/01	Connery et al.	370	392	
	AS	6,226,680	05/01/01	Boucher et al.	709	230	
	AT	6,247,060	06/12/01	Boucher et al.	709	238	
	AU	08/964,304		Napolitano et al.			

Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
 AV		WO 01/40960 A1 (PCT US00/32660)	06/07/01	International				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner <i>ML</i>	Date Considered <i>6/29/06</i>
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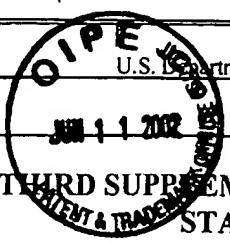
BA	"Two-Way TCP Traffic over Rate Controlled Channels: Effects and Analysis," by Lampros Kalampoukas, Anujan Varma and K.K. Ramakrishnan, IEEE Transactions on Networking, Volume 6, Number 6, 17 pages, December 1998.
BB	"Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design," iReady News Archives, 3 pages, downloaded November 2, 1998.
BC	"File System Design for an NFS File Server Appliance," by Dave Hitz, James Lau and Michael Malcolm, 12 pages, Copyright 1996.
BD	Adaptec press release, "Adaptec Announces EtherStorage Technology", 2 pages, June 14, 2000.
BE	Adaptec article entitled, "EtherStorage Frequently Asked Questions," 5 pages, July 19, 2000.
BF	Adaptec article entitled, "EtherStorage White Paper," 7 pages, July 19, 2000.
BG	Article entitled, "Computers; Storage," by James Berlino and Ananda Baruah, CIBC World Markets Equity Research, 9 pages, August 7, 2000.
BH	Merrill Lynch article entitled, "Storage Futures," by Steven Milunovich, 22 pages, May 10, 2000.
BI	Market Watch - News Articles, "Montreal start-up battles data storage bottleneck," by Susan Taylor, 2 pages, March 5, 2000.
BJ	Internet-Draft "SCSI/TCP (SCSI over TCP)," by J.Satran et al., 38 pages, February 2000.

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

2154



U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/970,124

Filing date: October 2, 2001

**THIRD SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Inventors: Peter K. Craft et al.

Group Art Unit: 2154

**NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES
SOLICITED SESSION LAYER READ COMMANDS**

Examiner name: Unknown

Attorney Docket No. ALA-020

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	6,345,301	02/05/02	Burns et al.	709	230	
	B						
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Foreign Patent Documents

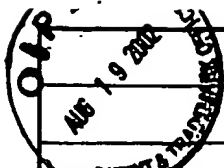
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		Document Number	Date	Country	Class	Subclass	Yes	No
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	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Examiner Date Considered 6/29/06

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.



U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/970,124

Filing date: October 2, 2001

Inventors: Peter K. Craft et al.

Group Art Unit: 2154

Examiner name: Unknown

Attorney Docket No. ALA-020

COPY OF PAPERS
ORIGINALLY FILEDFOURTH SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANTNETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES
SOLICITED SESSION LAYER READ COMMANDS

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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Foreign Patent Documents

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		Document Number	Date	Country	Class	Subclass	Yes	No
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	N	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1987 (10 pages).
	O	Form 10-K for Exelan, Inc., for the fiscal year ending December 31, 1988 (10 pages).
	P	
	Q	

Examiner

Date Considered

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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/970,124

**FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Filing date: October 2, 2001

Inventors: Peter K. Craft, et al.

Group Art Unit: 2154

Examiner name: Unknown

Attorney Docket No. ALA-020

NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES
SOLICITED SESSION LAYER READ COMMANDS

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>MP</i>	A	5,058,110	10/15/91	Beach et al.	370	85.6	
	B	6,021,446	02/01/00	Gentry, Jr.	709	303	
	C	6,356,951	03/12/02	Gentry, Jr.	709	250	
	D	6,389,468	05/14/02	Muller et al.	709	226	
	E	6,427,169	07/30/02	Elzur	709	224	
	F	6,434,651	08/13/02	Gentry, Jr.	710	260	
<i>ML</i>	G	6,449,656	09/10/02	Elzur et al.	709	236	
	H	6,453,360	09/17/02	Muller et al.	709	250	
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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
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	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>ML</i>	N	Article from Rice University entitled "LRP: A New Network Subsystem Architecture for Server Systems", by Peter Druschel and Gaurav Banga, Rice University, October 1996, 15 pages.
	O	Internet RFC/STD/FYI/BCP Archives article with heading "RFC2140" entitled "TCP Control Block Interdependence", web address http://www.faqs.org/rfcs/rfc2140.html , 9 pages, printed 9/20/02.
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Examiner

Date Considered

6/29/06

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U.S. Department of Commerce, Patent and Trademark Office

Application No.: 09/970,124

**SUPPLEMENTAL INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Filing date: October 2, 2001

Inventors: Peter K. Craft, et al.

Group Art Unit: 2154

Examiner name: Unknown

Attorney Docket No. ALA-020

NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES
SOLICITED SESSION LAYER READ COMMANDS

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
<i>MS</i>	A	5,598,410	1/28/1997	Stone	370	469	
	B						
	C						
	D						
	E						
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Foreign Patent Documents

							Translation	
		Document Number	Date	Country	Class	Subclass	Yes	No
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	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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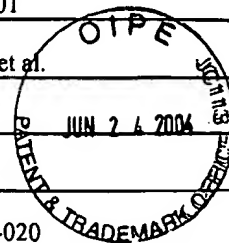
Examiner

Date Considered

6/29/06

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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/970,124
7th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 2, 2001
	Inventors: Peter K. Craft, et al.
	Group Art Unit: 2154
NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS	Examiner name: Unknown
	Attorney Docket No. ALA-020



U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,517,668	5/14/1996	Szwerinski et al.	395	800
	B					
	C					
	D					
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Foreign Patent Documents

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		Document Number	Date	Country	Class	Subclass	Yes	No
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	M							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Examiner	Date Considered <u>6/29/06</u>
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>	

U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/970,124
8 th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 2, 2001
	Inventors: Peter K. Craft, et al.
	Group Art Unit: 2154
NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS	Examiner name: Unknown
	Attorney Docket No. ALA-020

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A	5,524,250	6/4/96	Chesson et al.	395	775	
	B	5,619,650	4/8/97	Bach et al.	395	200.01	
	C	5,727,142	3/10/98	Chen	395	181	
	D	5,802,258	9/1/98	Chen	395	182.08	
	E	5,898,713	4/27/99	Melzer et al.	371	53	
	F	6,021,507	2/1/00	Chen	714	2	
	G	6,047,323	4/4/00	Krause	709	227	
	H						

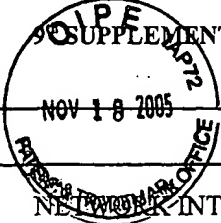
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*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	1	Schwaderer et al., IEEE Computer Society Press publication entitled, "XTP in VLSI Protocol Decomposition for ASIC Implementation", from 15 th Conference on Local Computer Networks, 5 pages, Sept. 30 – Oct. 3, 1990.
	2	Beach, Bob, IEEE Computer Society Press publication entitled, "UltraNet: An Architecture for Gigabit Networking", from 15 th Conference on Local Computer Networks, 18 pages, Sept. 30 – Oct. 3, 1990.
	3	Chesson et al., IEEE Symposium Record entitled, "The Protocol Engine Chipset", from Hot Chips III, 16 pages, Aug. 26-27, 1991.
	4	Maclean et al., IEEE Global Telecommunications Conference, Globecom '91, presentation entitled, "An Outboard Processor for High Performance Implementation of Transport Layer Protocols", 7 pages, Dec. 2-5, 1991.
	5	Ross et al., IEEE article entitled "FX1000: A high performance single chip Gigabit Ethernet NIC", from Compcon '97 Proceedings, 7 pages, Feb. 23-26, 1997.
	6	Strayer et al., "Ch. 9: The Protocol Engine" from XTP: The Transfer Protocol, 12 pages, July 1992.
	7	Publication entitled "Protocol Engine Handbook", 44 pages, Oct. 1990.
	8	Koufopavlou et al., IEEE Global Telecommunications Conference, Globecom '92, presentation entitled, "Parallel TCP for High Performance Communication Subsystems", 7 pages, Dec. 6-9, 1992.
	9	Lilienkamp et al., Publication entitled "Proposed Host-Front End Protocol", 56 pages, Dec. 1984.

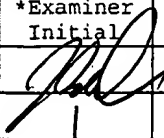

Examiner

Date Considered

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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/970,124
	Filing date: October 2, 2001
	Inventors: Peter K. Craft, et al.
NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS	Group Art Unit: 2154
	Examiner name: Larry D. Donaghue
	Attorney Docket No. ALA-020

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	1	4,589,063	5/13/86	Shah et al.	710	8	
	2	5,642,482	6/24/97	Pardillos	395	200.2	
	3	5,701,516	12/23/97	Cheng et al.	395	842	
	4	5,778,013	7/7/98	Jedwab	714	807	
	5	5,809,328	09/15/98	Nogales et al.	395	825	
	6	5,931,918	08/03/99	Row et al.	709	300	
	7	5,935,205	08/10/99	Murayama et al.	709	216	
	8	5,937,169	08/10/99	Connery et al.	395	200.8	
	9	5,941,969	08/24/99	Ram et al.	710	128	
	10	5,941,972	08/24/99	Hoese et al.	710	129	
	11	5,950,203	09/07/99	Stakuis et al.	707	10	
	12	5,970,804	10/26/99	Osborne	395	200.75	
	13	5,996,024	11/30/99	Blumenau	709	301	
	14	6,016,513	01/18/00	Glen H. Lowe	709	250	
	15	6,026,452	02/15/00	William Michael Pitts	710	56	
	16	6,047,356	04/04/00	Anderson et al.	711	129	
	17	6,057,863	05/02/00	Sompong P. Olarig	345	520	
	18	6,065,096	05/16/00	Day et al.	711	114	
	19	6,067,569	5/23/00	Khaki et al.	709	224	
	20	6,070,200	5/30/00	Gates et al.	710	20	
	21	6,101,555	8/8/00	Goshey et al.	709	321	
	22	6,141,705	10/31/00	Anand et al.	710	15	
	23	6,145,017	11/7/00	Ghaffari	710	5	
	24	6,157,955	12/5/00	Narad et al.	709	228	
	25	6,172,980	1/9/01	Flanders et al.	370	401	
	26	6,202,105	3/13/01	Gates et al.	710	20	
	27	6,226,680	05/01/01	Boucher et al.	709	230	

Examiner 	Date Considered <u>1/29/06</u>
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U.S. Department of Commerce, Patent and Trademark Office	Application No.: 09/970,124
9 th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Filing date: October 2, 2001
	Inventors: Peter K. Craft, et al.
	Group Art Unit: 2154
NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS	Examiner name: Larry D. Donaghue
	Attorney Docket No. ALA-020

U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	28	6,246,683	06/12/01	Connery et al.	370	392	
	29	6,247,060	06/12/01	Boucher et al.	709	238	
	30	6,279,051	8/21/01	Gates et al.	710	20	
	31	6,298,403	10/2/01	Suri et al.	710	100	
	32	2001/0053148	12/20/01	Bilic et al.	370	389	
	33	2001/0048681	12/6/01	Bilic et al.	370	389	
	34	2001/0014954	8/16/01	Purcell et al.	714	4	
	35	2001/0014892	8/16/01	Gaither et al.	707	200	
	36	2001/0013059	8/9/01	Dawson et al.	709	217	

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		Document Number	Date	Country	Class	Subclass	Yes	No
	37	WO 00/13091	3/09/00	PCT/US98/24943				
	38	WO 01/40960	6/07/01	PCT/US00/32660				
	39	WO 01/59966	8/16/01	PCT/US00/06475				
	40	WO 01/86430	11/15/01	PCT/US01/15180				



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*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	41	Internet pages of Xpoint Technologies, Inc. entitled "Smart LAN Work Requests", 5 pages, printed 12/19/97.
	42	Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).
	43	Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.
Examiner	Date Considered 6/29/06	

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	Inventors: Peter K. Craft, et al.
	Group Art Unit: 2154
NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS	Examiner name: Larry D. Donaghue
	Attorney Docket No. ALA-020

OTHER ART—NON PATENT LITERATURE DOCUMENTS

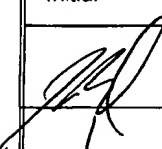
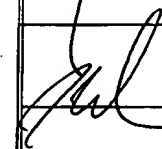
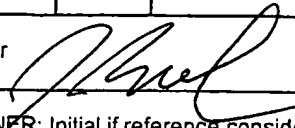
*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	44	Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996, 15 pages.
	45	Internet pages directed to Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.
	46	VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.
	47	"Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, December 1998, 17 pages.
	48	IReady News article entitled "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design", Santa Clara, CA, and Tokyo, Japan, October 14, 1998, printed 11/2/98, 3 pages.
	49	"File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.
	50	Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.
	51	Adaptec article entitled "EtherStorage Frequently Asked Questions", 5 pages, printed 7/19/00.
	52	Adaptec article entitled "EtherStorage White Paper", 7 pages, printed 7/19/00.
	53	CIBC World Markets article entitled "Computers; Storage", by J. Berlino et al., 9 pages, dated August 7, 2000.
	54	Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.
	55	CBS Market Watch article entitled "Montreal Start-Up Battles Data Storage Bottleneck", by S. Taylor, dated March 5, 2000, 2 pages, printed 3/7/00.
	56	Internet-draft article entitled "SCSI/TCP (SCSI over TCP)", by J. Satran et al., 38 pages, dated February 2000, printed 5/19/00.
	57	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
	58	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.

Examiner

Date Considered

6/29/06

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U.S. Department of Commerce, Patent and Trademark Office		Application No.: 09/970,124
9th SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing date: October 2, 2001
		Inventors: Peter K. Craft, et al.
NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS		Group Art Unit: 2154
		Examiner name: Larry D. Donaghue
		Attorney Docket No. ALA-020
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*Examiner Initial	Cite No.	(Including Author, Title, Date, Pertinent Pages, Etc.)
	59	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.
	60	Thla, Y.H. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control", <u>Protocols for High Speed Networks</u> , pages 53-68, 1993.
	61	Thla, Y.H. Publication entitled "A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture", <u>Protocols for High Speed Networks</u> , pages 224-239, 1995.
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Examiner 	Date Considered <u>6/29/06</u>	
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